



Description

Receiver RX 1001 M / RX 5001

Abbreviation index

AC	Alternating Current
AGC	Automatic Gain Control
BCD	Binary Coded Decimal
BFO	Beat Frequency Oscillator
BITE	Built In Test Equipment
CCITT	Comité Consultatif International Télégraphique et Téléphonique
CPU	Central Processing Unit
CW	Continuous Wave
DC	Direct Current
DSB	Double Side Band
EMC	Electromagnetic Compatibility
EMF	Electromotiv Force
EPROM	Erasable Programmable ROM
FM	Frequency Modulation
HF	High Frequency
IC	Integrated Circuit
IF	Intermediate Frequency
ISB	Independant Side Band
LED	Light Emitting Diode
LF	Low Frequency
LSB	Lower Side Band
max.	maximum
MGC	Manual Gain Control
min.	minimum
PIO	Parallel Input/Output port
PLL	Phase Locked Loop
RAM	Random Access Memory
ROM	Read Only Memory
RX	Receiver

SINAD	Signal to Noise ratio And Distortion
SSB	Single Side Band
SW	Width Across Flats
TCXO	Temperature Compensated crystal (X-tal) Oscillator
TTL	Transistor - Transistor Logic
typ.	typical
USB	Upper Side Band
VCO	Voltage Controlled Oscillator
VDE	(Verband Deutscher Elektrotechniker) Association of German Electrotechnical Engineers
VLF	Very Low Frequency

RX 1001 M / RX 5001

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